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(to be used for all correspondence after initial filing)			Examiner i	Name	Bharat Barot			
Total Number of Pages in This	1	Attorney D	ocket Number	2207/1	1659			
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Date		February 8, 2008 Reg. No.		56,022				
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PATENT Att'y Dkt: 2207/11659

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Anil VASUDEVAN

Serial No.: 09/893,888

Filed: June 29, 2001

For METHOD AND APPARATUS TO

REDUCE PACKET TRAFFIC

ACROSS AN I/O BUS

Assignee: Intel Corporation

Examiner: Bharat Barot

Confirmation No. 4965

Art Unit: 2155

AMENDMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

In response to the Office Action mailed November 13, 2007 in the aboveidentified application, the Applicant respectfully submits the following.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 6 of this paper.